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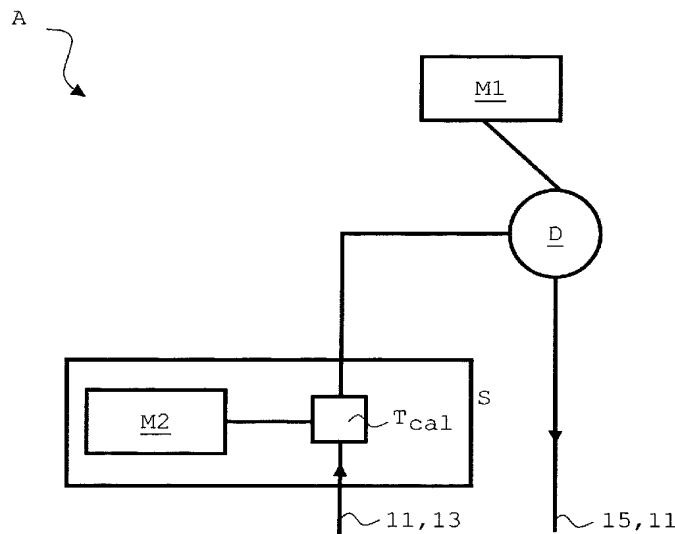
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(54) Title: ELECTRICAL CIRCUIT ARRANGEMENT FOR A DISPLAY DEVICE



(57) Abstract: The electrical circuit arrangement (A) for a display device (6) comprises an input terminal (11;13) a first memory element (M1); a driver element (D) coupled to the first memory element (M1); and a calibration circuit (S) coupled between the driver element (D) and the input terminal (11; 13). Information about first signal ( $I_{\text{prog}}$ ;  $I_{\text{dat}}$ ) received via the input terminal (11; 13) is stored in the first memory element (M1). The driver element provides a second signal ( $I_{\text{light}}$ ;  $I_{\text{prog}}$ ) via an output terminal (15; 11) in accordance with the information as stored in the first memory element (M1). The calibration circuit (S) matches a potential difference between the driver element (D) and the input terminal (11;13) during a calibration phase prior to receiving the first signal ( $I_{\text{prog}}$ ;  $I_{\text{dat}}$ ).

WO 2005/091269 A2



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